

NOV 30 2005

LSI LOGIC

FAX

LSI Logic Corporation  
Legal Department - IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035Date November 30, 2005Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2121

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## REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/719,673  
Filing date: November 21, 2003  
First named inventor: Khosro Khakzadi et al.  
Attorney docket number: 03-1862 / L13.12-0251

Transmitted herewith for filing via facsimile:

- Status Request Letter

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NQV 3 0 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of :**

Khosro Khakzadi et. al.

**Serial No. :**

10/719,673

**Group Art Unit :**      2121

**Filed :**

November 21, 2003

**Examiner :**

**For :**

## Chip Design Command Processor

**Atty Docket :** L13.12-0251 / 03-1862

## STATUS REQUEST

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**Sir:**

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

**LSI Logic Corporation**  
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Date: 11/20/5

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